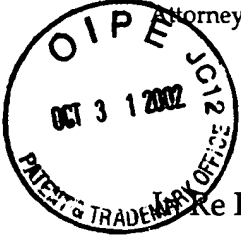


10-31-02

2123  
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Attorney's Docket No. 004120.P005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:

Jengsheng Jason Cong et al.

Examiner: Unknown

Application No.: 09/823,085

Art Unit: 2123

Filed: March 29, 2001

For: Method and Apparatus for  
Calculation of Crosstalk Noise in  
Integrated Circuits

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Technology Center 2100

Commissioner for Patents  
Washington, DC 20231

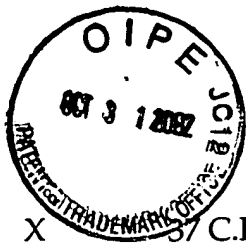
INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):



X 37 C.F.R. §1.97(b); OR

\_\_\_\_\_ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

\_\_\_\_\_ A statement pursuant to 37 C.F.R. §1.97(e) or

\_\_\_\_\_ A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p); OR

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\_\_\_\_\_ STATEMENT UNDER 37 C.F.R. §1.97(e) I hereby certify: \_\_\_\_\_

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\_\_\_\_\_ That each item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement, OR


\_\_\_\_\_ That no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned attorney after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in §1.56(c) more than three months prior to the filing the Information Disclosure Statement.

(2) A check for \$180.00 for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.  
Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 10/20/02

  
Michael J. Mallie, Reg. No. 36,591

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Los Angeles, CA 90025-1026  
(408) 720-8300

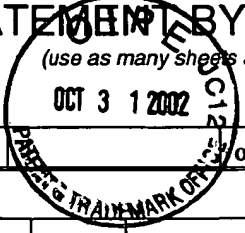
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, Washington, DC 20231 on

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Substitute Form 1449A/PTO (Modified)  <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(use as many sheets as necessary)</p> <div style="text-align: center;">  </div>		<div style="text-align: right; font-weight: bold;">Complete if Known</div> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>09/823,085</td> </tr> <tr> <td>Filing Date</td> <td>March 29, 2001</td> </tr> <tr> <td>First Named Inventor:</td> <td>Jingsheng Jason Cong</td> </tr> <tr> <td>Group Art Unit</td> <td>2123</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> <tr> <td>Attorney Docket Number</td> <td>004120.P005</td> </tr> </table>		Application Number	09/823,085	Filing Date	March 29, 2001	First Named Inventor:	Jingsheng Jason Cong	Group Art Unit	2123	Examiner Name	Unknown	Attorney Docket Number	004120.P005
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	A	KAWAGUCHI, H. et al.: "Delay and Noise Formulas for Capacitively Coupled Distributed RC lines", in Proc. Asia and South Pacific Design Automation Conf., pp. 35-43, 1998.													
	B	NAKAGAWA, S. et al.: "On-chip Cross Talk Noise Model for Deep-Submicrometer ULSI Interconnect", Hewlett-Packard Journal, vol. 49, pp. 39-45, Aug. 1998.													
	C	STÖHR, T. et al.: "Analysis, Reduction and Avoidance of Crosstalk on VLSI Chips", in Proc. Int. Symp. On Physical Design, pp. 211-218, April 1998.													
	D	VITTAL, A. et al.: "Crosstalk Reduction for VLSI", IEEE Trans. On Computer-aided Design of Integrated Circuits and Systems", vol. 16, pp. 290-98, 1997.													
	E	DEVGAN, A. : "Efficient Coupled Noise Estimation for On-chip Interconnects", in Proc. IEEE/ACM Int. Conf. On Computer Aided Design, pp. 147-153, 1997.													
	F	SEMICONDUCTOR INDUSTRY ASSOCIATION, "The National Technology Roadmap for Semiconductors", 1997 edition.													
	G	SHEPARD, K.L., et al.: "Noise in Deep Submicron Digital Design", in Proc. IEEE/ACM Int. Conf. On Computer Aided Design, pp. 524-531, 1996.													
	H	SAKURAI, T.: "Closed-form Expressions for Interconnection Delay, Coupling, and Crosstalk in VLSIs", IEEE Trans. On Electron Devices, vol. 40, pp. 118-124, 1993.													

Examiner Signature	Date Considered	
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